

SR8ProX

A DVB-S/S2/S2X Demodulator, with eight (8) channels and two (2) RF inputs, equipped with an advanced Front-End capable of handling symbol rates from as low as 0.3Msps to as high as 45Msps.



Product Highlights

- Eight (8) demodulators
- Two (2) RF inputs
- Full L-band tuner 950 – 2150 MHz
- Broadcast and Interactive DVB-S2X Profiles
- Full CCM, VCM, ACM standard compliance
- Roll Off down to 0.05 for DVB-S2X and 0.20 for DVB-S2
- IP output over GbE interface
- Fast lock time – power up to data output
- Advanced lock and search modes:
 - L-Band frequency only with auto symbol rate detection
 - L-Band + symbol rate
 - Relock (no search)
- 14/18V, 22KHz, 500ma LNB power (support universal and professional LNB)

Product Specifications

Receiver

DVB-S/S2/S2X including Annex-M (in single channel mode)

Roll-off factors 0.05, 0.10, 0.15, 0.20, 0.25, 0.35

Signal Level -30 to -60 dBm

Symbol Rates 0.3Msps to 45Msps

Input connector SMA, 50 Ohms

LNB power 14/18V, 22KHz

Flexible routing of RF input to Demodulator

Up to 8 channels with 45Mbps each in multiple channel mode.

Hardware processing

FPGA based

De-GSE

GbE Traffic Interface

Environmental Conditions

Operating Temp. 0° to 50° C.

Storage Temp. -25° to +70° C

Safety

CE

EMI/EMC FCC part 15, Class A

Power

110-240 AC, 12W max

Control & Monitor

Serial port Serial over USB CLI GbE interface

Web UI and SNMP

Static IP address or DHCP

Upgrade

TFTP based SW and FW

2 images saved in internal Flash

Dimensions

1RU rack mount

Weight

1.8 Kg

DVB-S supported rates: 1/2, 2/3, 3/4, 5/6, 7/8

DVB-S2 ModCodes:

#	Mode	#	Mode	#	Mode	#	Mode
1	QPSK 1/4	9	QPSK 5/6	17	8PSK 9/10	25	32APSK 4/5
2	QPSK 1/3	10	QPSK 8/9	18	16APSK 2/3	26	32APSK 5/6
3	QPSK 2/5	11	QPSK 9/10	19	16APSK 3/4	27	32APSK 8/9
4	QPSK 1/2	12	8PSK 3/5	20	16APSK 4/5	28	32APSK 9/10
5	QPSK 3/5	13	8PSK 2/3	21	16APSK 5/6	29	Reserved
6	QPSK 2/3	14	8PSK 3/4	22	16APSK 8/9	30	Reserved
7	QPSK 3/4	15	8PSK 5/6	23	16APSK 9/10	31	Reserved
8	QPSK 4/5	16	8PSK 8/9	24	32APSK 3/4	0	DUMMY PLFRAME

DVB S2x PLS Codes:

PLS Code	Modulation	Code Rate	Frame Length	PLS Code	Modulation	Code Rate	Frame Length
132	QPSK	13\45	Normal	194	64APSK	4\5	Normal
134	QPSK	9\20	Normal	198	64APSK	5\6	Normal
136	QPSK	11\20	Normal	200	128APSK*	3\4	Normal
138	8APSK	5\9-L	Normal	202	128APSK*	7\9	Normal
140	8APSK	26\45-L	Normal	204	256APSK*	29\45-L	Normal
142	8PSK	23\36	Normal	206	256APSK*	2\3-L	Normal
144	8PSK	25\36	Normal	208	256APSK*	31\45-L	Normal
146	8PSK	13\18	Normal	210	256APSK*	32\45	Normal
148	16APSK	1\2-L	Normal	212	256APSK*	11\15-L	Normal
150	16APSK	8\15-L	Normal	214	256APSK*	3\4	Normal
152	16APSK	5\9-L	Normal	216	QPSK	11\45	Short
154	16APSK	26\45	Normal	218	QPSK	4\15	Short
156	16APSK	3\5	Normal	220	QPSK	14\45	Short
158	16APSK	3\5-L	Normal	222	QPSK	7\15	Short
160	16APSK	28\45	Normal	224	QPSK	8\15	Short
162	16APSK	23\36	Normal	226	QPSK	32\45	Short
164	16APSK	2\3-L	Normal	228	8PSK	7\15	Short
166	16APSK	25\36	Normal	230	8PSK	8\15	Short
168	16APSK	13\18	Normal	232	8PSK	26\45	Short
170	16APSK	7\9	Normal	234	8PSK	32\45	Short
172	16APSK	77\90	Normal	236	16APSK	7\15	Short
174	32APSK	2\3-L	Normal	238	16APSK	8\15	Short
178	32APSK	32\45	Normal	240	16APSK	26\45	Short
180	32APSK	11\15	Normal	242	16APSK	3\5	Short
182	32APSK	7\9	Normal	244	16APSK	32\45	Short
184	64APSK	32\45-L	Normal	246	32APSK	2\3	Short
186	64APSK	11\15	Normal	248	32APSK	32\45	Short
190	64APSK	7\9	Normal				

Disclaimer: The SR8ProX is based on STMicro STiD135 and is limited by the chip limitations.